INTERNATIONAL SEARCH REPORT

International application No.

PCT/US03/34702

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| | SSIFICATION OF SUBJECT MATTER : G06F 17/50; 1/08, 1/04 | | | |
| IPC(7) : G06F 17/50; 1/08, 1/04 US CL : 716/1; 327/291, 407, 115 | | | | |
| | International Patent Classification (IPC) or to both n | ational classification and IPC | | |
| | DS SEARCHED | | <u>-</u> - | |
| Minimum do | ocumentation searched (classification system followed | by classification symbols) | | |
| | /16/1; 327/291, 407, 115 | oy audom-110 by 11.00.0) | | |
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| Documentati | on searched other than minimum documentation to the | e extent that such documents are included i | n the fields seembed | |
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| | ata base consulted during the international search (nan | ne of data base and, where practicable, sear | rch terms used) | |
| Please see C | Continuation Sheet | | | |
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| | UMENTS CONSIDERED TO BE RELEVANT | | | |
| Category * | Citation of document, with indication, where a | | Relevant to claim No. | |
| Α | US 3,864,556 A (FLUET) 04 Feburary 1975 (04.02 | 2.1975), see entire document. | 1-13, 19, 20 | |
| x | US 6,466,073 B1 (YUKINARI et al) 15 October 20 | 102.(15.10.2002) shetract: Figures 4. 7 | 14, 15 | |
| | 8; column 2, line 61 - column 3, line 28; column 9, | line 1 - column 10, line 45. | 14, 15 | |
| Y | | | 16, 17, 18 | |
| *** | WOOTHING D. I. G. J. G. | | | |
| Y | KOZIEROK, Derived System Clocks, The PC Guide, 17 April 2001 (17.04.2001), Version 16, 17 2.2.0, pages 1-2. | | | |
| Y | US 5,047,733 A (NONAKA et al) 10 September 20 | 01 (10 09 2001) Figures 1 5: column | 18 | |
| - | 1, lines 20-37; column 4, line 67 - column 5, line 2 | 8. | 10 | |
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| | documents are listed in the continuation of Box C. | See patent family annex. | | |
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| | defining the general state of the art which is not considered to be | date and not in conflict with the application principle or theory underlying the inve | ation but cited to understand the | |
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| | | being obvious to a person skilled in the | art | |
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| | ctual completion of the international search | Date of mailing of the international accord | | |
| | • | Date of mailing of the international search report | | |
| 30 March 2004 (30.03.2004) | | 29 APR 2004 | | |
| Name and mailing address of the ISA/US Mail Stop PCT, Attn: ISA/US | | Authorized officer | _ | |
| Commissioner for Patents | | Sun Lin | | |
| | . Box 1450 xandria, Virginia 22313-1450 | Telephone No. (571) 272-1899 | $O \sim 1$ | |
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| G (1 (1 A) AN TOTAL DOCUMENTS. | |
| Continuation of B. FIELDS SEARCHED Item 3: | |
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| USPAT, US-PGPUB, EPO, JPO, DERWENT, IBM_TDB, IEEE, GOOGLE | NY 1'11 DETER |
| USPAT, US-PGPUB, EPO, JPO, DERWENT, IBM_TDB, IEEE, GOOGLE search terms: frequency scaling, synchronous, quiescent/quiet bus state, clock frequency, clock bus clock processor. IC reference frequency. | equency change, PLL, divider, multiplexer/MUX, |
| USPAT, US-PGPUB, EPO, JPO, DERWENT, IBM_TDB, IEEE, GOOGLE search terms: frequency scaling, synchronous, quiescent/quiet bus state, clock fre processor clock, bus clock, processor, IC, reference frequency | equency change, PLL, divider, multiplexer/MUX, |
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